

Product Features

- 4 Parallel lanes design
- Compliant with QSFP+MSA
- Management interface specifications per SFF-8436
- 4 channels PIN photo detector
- Up to 11.2Gb/s per channel data links
- Single +3.3V power supply
- Class 1 laser safety certified
- Commercial operating temperature:0°C to +70°C
- Up to 10km on SMF
- RoHS Compliant



Applications

- 40GBASE-LR4 Ethernet
- Infiniband QDR,DDR and SDR
- Data center

Descriptions

QSFP-40G-PSM-LR4 QSFP+ transceivers are designed for use in 40Gb/s links over single mode fiber. They integrates four independent transmit and receive channels, Each channel operate at 10.3125G/s, the module can operate at 40Gb/s up to 10km using 9/125um SMF. They are compliant with the QSFP+ MSA and IEEE 802.3ba 40GBASE-LR4,the optical fiber ribbon cable with the MPO(MTP) connector. QSFP-40G-PSM-LR4 are compliant with RoHS.

Ordering Information

Table 1. Ordering Information

Part Number	Transmitter	Output Power	Receiver	Sensitivity	Reach	Temp	DDM	RoHS
QSFP-40G-PSM-LR4	1310nm DFB	-6.2 ~ 0.5dBm	PIN	< -12.6dBm	10km	0~ 70 °C	Available	Compliant

Pin Description

Table 2. Pin Description

Pin	Name	Function/Description	Notes
1	GND	Transmitter Ground (Common with Receiver Ground)	1
2	Tx2-	Transmitter Inverted Data Input	
3	Tx2+	Transmitter Non-Inverted Data output	
4	GND	Transmitter Ground (Common with Receiver Ground)	1
5	Tx4-	Transmitter Inverted Data Input	
6	Tx4+	Transmitter Non-Inverted Data output	
7	GND	Transmitter Ground (Common with Receiver Ground)	1
8	ModSelL	Module Select	2
9	ResetL	Module Reset	2
10	VccRx	3.3V Power Supply Receiver	
11	SCL	2-Wire serial Interface Clock	2
12	SDA	2-Wire serial Interface Data	2
13	GND	Transmitter Ground (Common with Receiver Ground)	1
14	Rx3+	Receiver Non-Inverted Data Output	
15	Rx3-	Receiver Inverted Data Output	
16	GND	Transmitter Ground (Common with Receiver Ground)	1
17	Rx1+	Receiver Non-Inverted Data Output	
18	Rx1-	Receiver Inverted Data Output	
19	GND	Transmitter Ground (Common with Receiver Ground)	1
20	GND	Transmitter Ground (Common with Receiver Ground)	1
21	Rx2-	Receiver Inverted Data Output	
22	Rx2+	Receiver Non-Inverted Data Output	
23	GND	Transmitter Ground (Common with Receiver Ground)	1
24	Rx4-	Receiver Inverted Data Output	1
25	Rx4+	Receiver Non-Inverted Data Output	
26	GND	Transmitter Ground (Common with Receiver Ground)	1
27	ModPrsl	Module Present	
28	IntL	Interrupt	2
29	VccTx	3.3V power supply transmitter	
30	Vcc1	3.3V power supply	
31	LPMode	Low Power Mode	2
32	GND	Transmitter Ground (Common with Receiver Ground)	1
33	Tx3+	Transmitter Non-Inverted Data Input	
34	Tx3-	Transmitter Inverted Data Output	
35	GND	Transmitter Ground (Common with Receiver Ground)	1
36	Tx1+	Transmitter Non-Inverted Data Input	
37	Tx1-	Transmitter Inverted Data Output	
38	GND	Transmitter Ground (Common with Receiver Ground)	1

Notes:

1. The module signal grounds are isolated from the module case.
2. This is an open collector/drain output that on the host board requires a 4.7KΩ to 10KΩ pull-up resistor to VccHost.

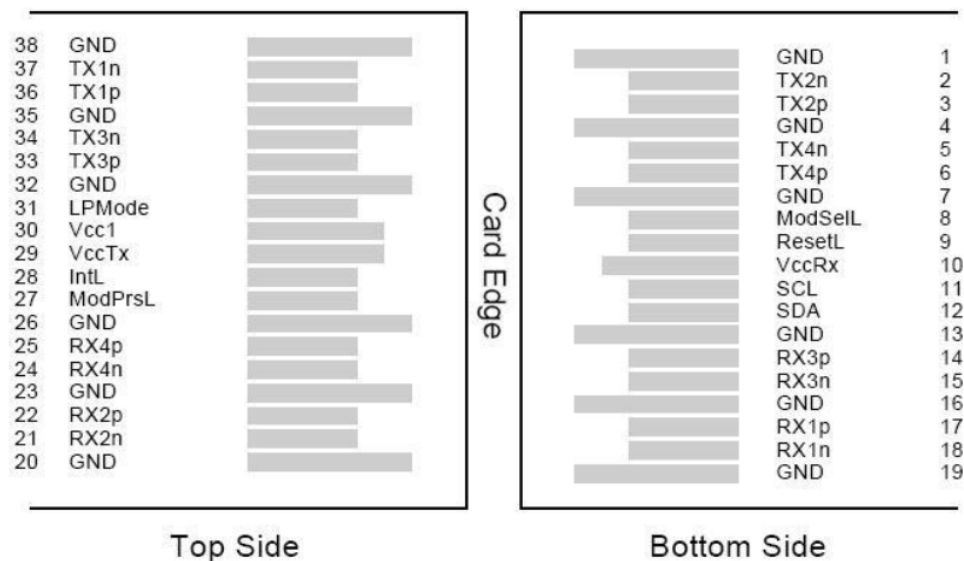


Figure 1. Host PCB QSFP+ pad assignment top view

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Table 3. Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Unit
Storage Temperature	T _S	-40	85	°C
Relative Humidity	RH	5	95	%
Supply Voltage	V _{CC}	-0.5	4.0	V

Recommended Operating Conditions

Table 4. Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Operating Case Temperature	T _C	0	25	70	°C
Supply Voltage	V _{CC}	3.135	3.3	3.465	V
Data Rate PER Channel	-	-	10.3125	11.2	Gb/s

Transceiver Electrical Characteristics

Table 5. Transceiver Electrical Characteristics

Parameter		Symbol	Minimum	Typical	Maximum	Unit	Notes
Module Supply Current		I _{CC}	-	-	1100	mA	-
Power Dissipation		P _D	-	-	3500	mW	-
Transmitter							
Input Differential Impedance		Z _{IN}	-	100	-	Ω	-
Differential Data Input Swing		V _{IN, P-P}	180	-	900	mV _{P-P}	-
TX_FAULT	Transmitter Fault	V _{OH}	2.0	-	V _{CCHOST}	V	-
	Normal Operation	V _{OL}	0	-	0.8	V	-
TX_DISABLE	Transmitter Disable	V _{IH}	2.0	-	V _{CCHOST}	V	-
	Transmitter Enable	V _{IL}	0	-	0.8	V	-
Receiver							
Output Differential Impedance		Z _O	-	100	-	Ω	-
Differential Data Output Swing		V _{OUT, P-P}	300	-	850	mV _{P-P}	1
Data Output Rise Time, Fall Time		t _r , t _f	28	-	-	ps	2
RX_LOS	Loss of signal (LOS)	V _{OH}	2.0	-	V _{CCHOST}	V	3
	Normal Operation	V _{OL}	0	-	0.8	V	3

Notes:

1. Internally AC coupled, but requires a external 100Ω differential load termination.
2. 20 – 80 %.
3. LOS is an open collector output. Should be pulled up with 4.7kΩ on the host board.

Transmitter Optical Characteristics

Table 6. Transmitter Optical Characteristics

Parameter		Symbol	Minimum	Typical	Maximum	Unit	Notes
Launch Optical Power per lane		P _o	-6.2	-	+0.5	dBm	1
Center Wavelength Range		λ ₀	1260	1310	1355	nm	-
Extinction Ratio		EX	3.5	-	-	dB	2
Spectral width(-20dB)		Δλ	-	-	1	nm	
Side Mode Suppression Ratio		SMSR	30	-	-	dB	
Optical Return Loss Tolerance		ORLT	-	-	12	dB	-
Pout @TX-Disable Asserted		P _{off}	-	-	-30	dBm	1
Eye Diagram			IEEE Std 802.3ba compatible				

Notes:

1. The optical power is launched into SMF.
2. Measured with a PRBS 2³¹-1 test pattern @10.3125Gbps.

Receiver Optical Characteristics

Table 7. Receiver Optical Characteristics

Parameter	Symbol	Minimum	Typical	Maximum	Unit	Notes
Center Wavelength	λ_c	1260	-	1340	nm	-
Receiver Sensitivity (OMA)	S	-	-	-12.6	dBm	1
Damage Threshold	P_{OL}	2.3	-	-	dBm	1
LOS De-Assert	LOS_D	-	-	-15	dBm	-
LOS Assert	LOS_A	-30	-	-	dBm	-
LOS Hysteresis	-	0.5	-	-	dB	-

Notes:

1. Measured with PRBS $2^{31}-1$ test pattern, 10.3125Gb/s, BER<10⁻¹².

Recommended Host Board Power Supply Filter Network

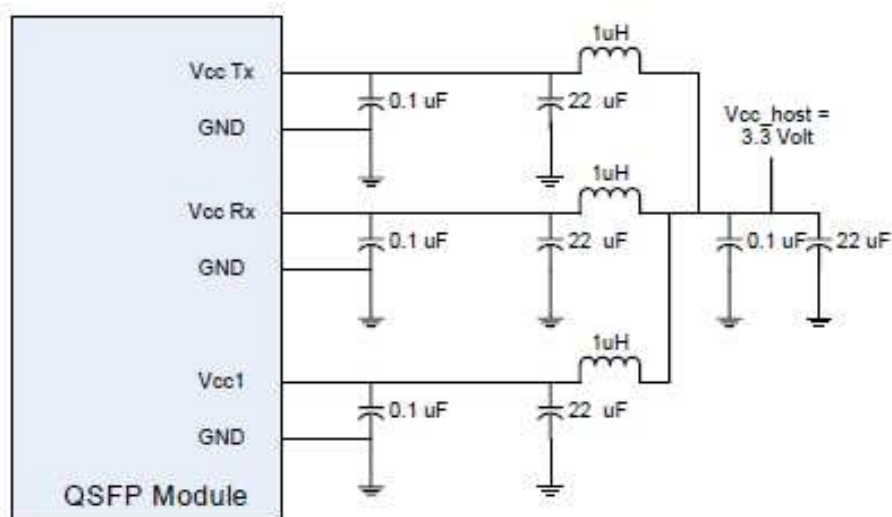


Figure 2. Recommended Host Board Power Supply Filter Network

Recommended Application Interface Block Diagram

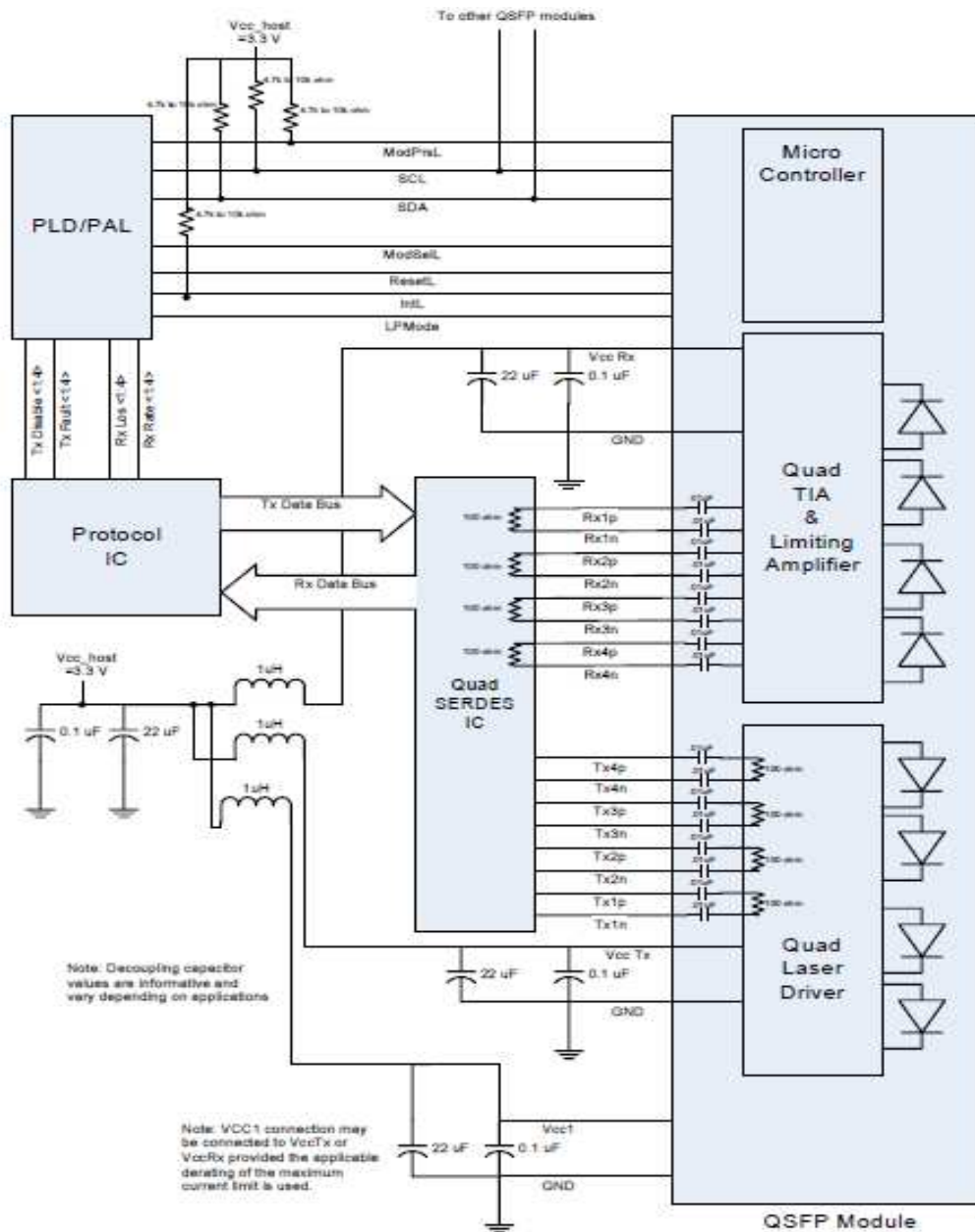


Figure 3. Recommended Application Interface Block Diagram

Mechanical specifications

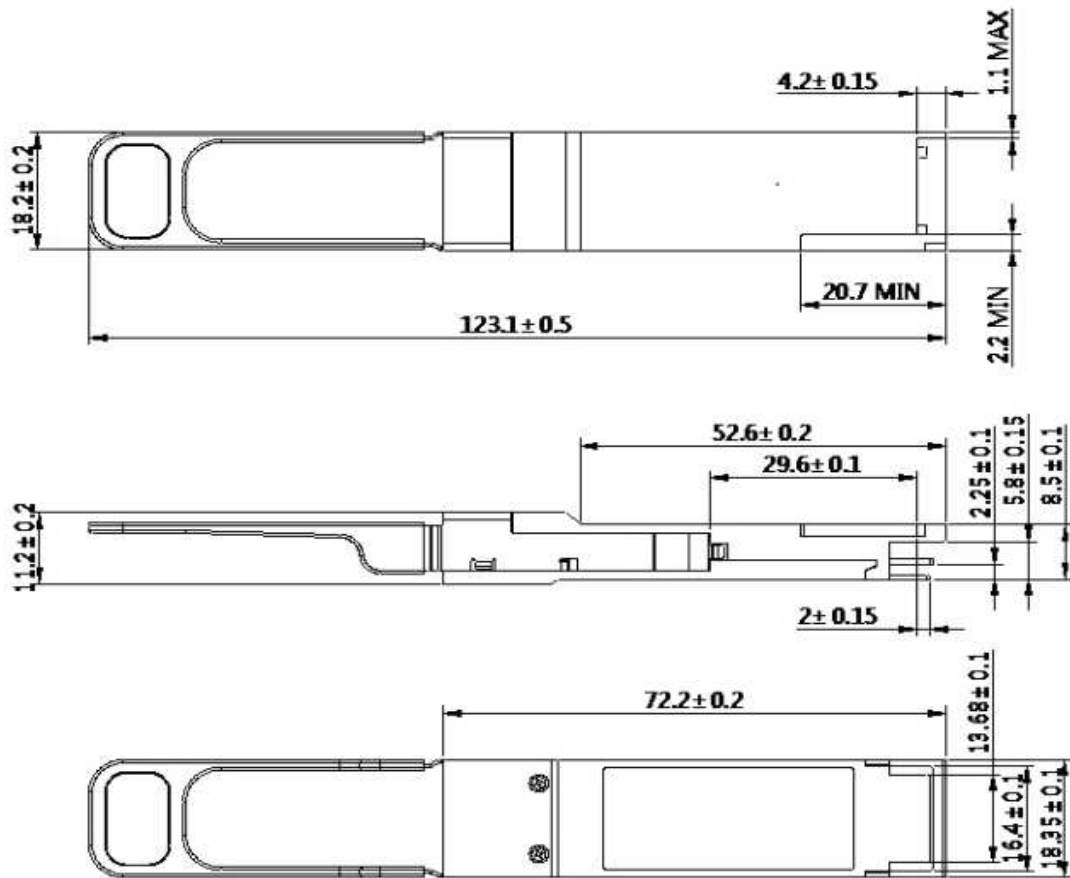


Figure 4. Outline Drawing

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